

The recently introduced concept of a combined resistive/capacitive coupling (RCC) scheme [1] offers a promising solution. RCC-SOH devices are fully compatible with standard silicon-photonics foundry processes, enable efficient poling via residual resistive coupling, and provide a nearly flat frequency response from 10 GHz to 110 GHz. This Thesis aims to further investigate and improve the new coupling scheme.

- Chip preparation
- Characterization of RCC modulators
- Electrical and optical simulation
- Modelling of the resistive/capacitive coupling scheme
- Design optimization

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